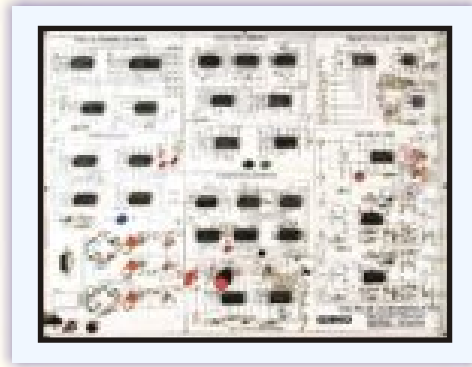




PULSE CODE DEMODULATION RECEIVER TRAINER

MODEL - COM204Z

This trainer has been designed with a view to provide practical and experimental knowledge of Pulse Code Demodulation technique as practically implemented in Digital Communication systems on a SINGLE P.C.B.



SPECIFICATIONS

1. Input Channel : 2 TDM Serial Input
2. Demodulation : Pulse Code Demodulation with Clock Regeneration by Phased Lock loop
3. Low Pass Filter : with cut off frequency in the range of 3 to 4 KHz
4. Operating Modes : Fast 240 KHz/Channel (approx.)
Slow 1 HZ/Channel (approx.)
5. Error Detection and Correction using Hamming Code or other method
6. PC to PC Communication facility with selectable Baud rate
7. Built-in Gaussian Noise Generation and Matched Filter
8. Various minimum necessary Test Point
9. 2 set of Manuals, Set of Patch Cord, Line Cord

EXPERIMENTS

1. Study of PCM Demodulation Technique,
2. Study of Time Division Demultiplexing of PCM Data.
3. Study of clock Regeneration by PLL.
4. Study of Detection & correction of error induced by switched faults in the transmitter.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-